

JAYAKRISHNAN MADATHIL WARRIEM

SENIOR SCIENTIST

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PROFILE

A researcher and practitioner in efforts aimed at using Information and Communication Technologies for Development of Education.

OBJECTIVE

To design and support effective integration of technology in education.

PROFESSIONAL EXPERIENCE

May '17 - May '18

Project Research Scientist in the Dept. of Computer Science Engineering, IIT Bombay in the IITBombayX project. In-charge of design and implementation of Faculty Development Programs

Jan - May, '17

Training Design Consultant for Solar Urja though Localization and Sustainability (SoULS) Project, IIT Bombay

May - August, '16

Co-instructor in AICTE approved faculty development programme on "Use of ICT in Education for Online and Blended Learning"

OTHER PROFESSIONAL EXPERIENCE

Sept '08 - Aug '09

External Consultant, Switch Specialist
Mindscape IT, Mashreq Bank, Dubai

Aug '06 - Sept '08

Software Engineer
Fidelity National Information Services (FIS)

AWARDS & CERTIFICATIONS

- *Mashruwala Award for Innovation in Educational Technology*, 2017
- Global certification of 'Certified Trainer and Facilitator' from Carlton Advanced Management Institute, Connecticut

EDUCATION

July 2012 - May 2018

Doctoral degree (PhD) from the Interdisciplinary Programme in Educational Technology (IDP in ET), IIT Bombay, India

2010 - 2012

M.Tech in *Technology & Development* from Centre for Technology Alternatives for Rural Areas (CTARA), IIT Bombay with CPI of 8.71

2002 - 2006

B.Tech in Electrical & Electronics Engineering from Mahatma Gandhi University, Kerala, with an aggregate of 78.21%

POSITIONS OF RESPONSIBILITY HELD

Oct. - Dec '16

Local Arrangement Co-Chair for 24th International Conference on Computers in Education and 8th IEEE International Conference on Technology for Education

Jul '14 - Jun '15

Academic Unit Representative for Academic Affairs (AURAA) from IDP in ET, IIT Bombay

PROFESSIONAL SOCIETY MEMBERSHIPS

2014 - onwards

Asia Pacific Society for Computers in Education (APSCE)

2013 - 2016

Institute of Electrical and Electronics Engineers (IEEE)

REFERENCES

1. Prof. Sahana Murthy, IDP in ET, IIT Bombay
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2. Prof. Deepak B Phatak, Dept. of CSE, IIT Bombay
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